

**Notice of References Cited**

Application/Control No.

10/535,452

Applicant(s)/Patent Under  
Reexamination  
OGAWA ET AL.

Examiner

Duy-Vu N. Deo

Art Unit

1765

Page 1 of 1

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*	B	US-3,674,658	07-1972	van Asperen et al.	205/50
*	C	US-4,420,365	12-1983	Lehrer, William I.	438/702
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, vol. 1, 1986, pages 407-408.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.